

Electronic Patent Application Fee Transmittal

Application Number:	10777261			
Filing Date:	12-Feb-2004			
Title of Invention:	Apparatus and method for performing static timing analysis of an integrated circuit design using dummy edge modeling			
First Named Inventor:	Craig Marshall Darsow			
Filer:	Derek P. Martin			
Attorney Docket Number:	ROC920030338US1			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Statutory disclaimer	1814	1	130	130
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				130